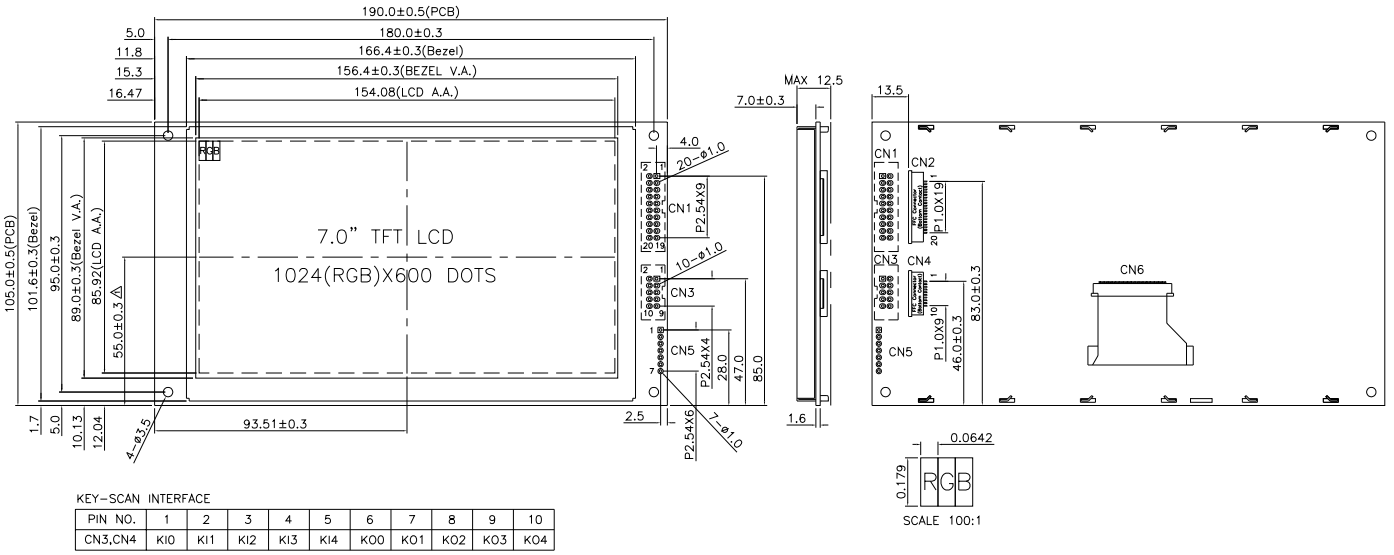
LT070D-33A

7", 1024(RGB) x 600 TFT, 8-bit parallel, SPI or I2C, 3.3V



KEY-SCAN INTERFACE

PIN NO.	1	2	3	4	5	6	7	8	9	10
CN3,CN4	KI0	KI1	KI2	KI3	KI4	KO0	KO1	KO2	KO3	KO4

SPI FLASH PROGRAMMING INTERFACE

PIN NO.	1	2	3	4	5	6	7
CN5	Vss	VDD	SFCLK	DI(IO0)	DO(IO1)	/SFCS1	TEST1

ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit
Supply Voltage(Logic)	V_{DD} - V_{SS}	-0.3	4.0	V
Supply Voltage(LED)	V_{LED} - V_{SS}	-0.3	9.0	V
Input Voltage	V_I	-0.3	V_{DD} + 0.3	V
Operating Temp.	T_{opr}	-20	70	°C
Storage Temp.	T_{stg}	-30	80	°C

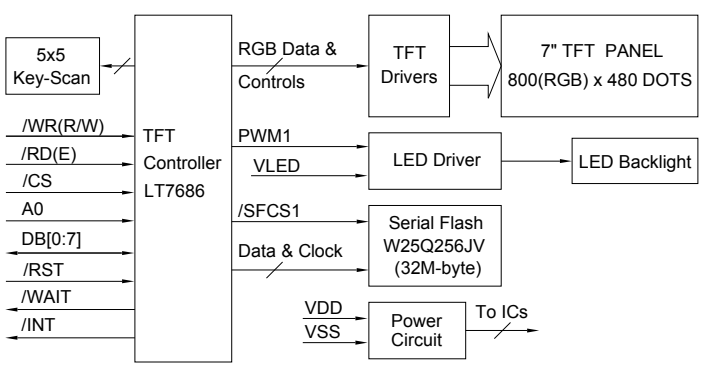
MECHANICAL DATA

Item	Nominal Dimensions	Unit
Module Size (W x H x T)	190.0 x 105.0 x 12.5	mm
Viewing Area (W x H)	156.4 x 89.0	mm
Active Area (W x H)	154.08 x 85.92	mm
Dot Pitch (W x H)	0.0642 x 0.179	mm
Weight	Approx. 230	g

ELECTRICAL CHARACTERISTICS (V_{DD}=3.3V±0.3V)

Item	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Input High Voltage	V_{IH}	--	2.0	--	V_{DD}	V
Input Low Voltage	V_{IL}	--	-0.3	--	0.8	V
Output High Voltage	V_{OH}	--	2.4	--	V_{DD}	V
Output Low Voltage	V_{OL}	--	0	--	0.4	V
Supply Current (Logic)	I_{DD}	V_{DD} = 3.3V	--	160	200	mA
Supply Current (LED)	I_{LED}	V_{LED} = 5.0V	--	300	360	mA
		V_{LED} = 9.0V	--	160	190	mA

BLOCK DIAGRAM



PIN CONNECTIONS (CN1/CN2)

Pin	Symbol	Level	Function
1	V_{SS}	0V	GND
2	V_{SS}	0V	GND
3	V_{DD}	3.3V	Power supply for logic
4	V_{LED}	4.5V to 9V	Power supply for LED B/L driver
5	/RST	L	Reset signal. Active "L".
6	/WAIT	L	Wait signal output. Active "L".
7	/INT	L	Interrupt signal output. Active "L".
8	NC	--	No connection
9	DB7	H/L	For parallel mode: DB0[7:0] are 8-bit data bus For SPI or I2C mode: DB7 is serial colck input (SCLK). DB6 is serial data input (SDI) for 4-wire SPI DB6 is bi-directional data (SDA) for I2C DB5 is bi-directional data (SD) for 3-wire SPI DB5 is serial data output (SDO) for 4-wire SPI DB5 is device address bit[5] for I2C DB4 is chip selection (/SCS) for 3/4-wire SPI DB4 is device address bit[4] for I2C DB[3:0] are device address bit[3:0] for I2C
10	DB6	H/L	
11	DB5	H/L	
12	DB4	H/L	
13	DB3	H/L	
14	DB2	H/L	
15	DB1	H/L	
16	DB0	H/L	
17	A0	H/L	Data or command selection H: Command L: Display data
18	/WR (R/W)	H/L	Write signal for 8080 MCU. R/W signal for 6800 MCU.
19	/RD (E)	H/L	Read signal for 8080 MCU. Enable signal for 6800 MCU.
20	/CS	L	Chip selection signal for parallel mode. Active "L".